#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 7,353,438 Issued: 4/1/2008

Name of Patentee: Wingyu Leung, et al.

Title of Invention: TRANSPARENT ERROR CORRECTING MEMORY

Attention: Certificate of Corrections Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO AND APPLICANT MISTAKE (37 C.F.R. section 1.323)

- 1. It is noted that errors appear in this patent of a typographical nature or character, as more fully described below. They occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination. A certificate of correction is requested.
- 2. Attached hereto is Form PTO-1050.
- 3. The exact page and line number where the error occurs in the application file are:

In the Office Action dated March 9, 2006, the Examiner overlooked Cited No. A6, GB 2289789 in the Information Disclosure Statement. We respectfully request that this reference be included on the above-referenced patent.

In the formal drawings filed August 20, 2003, Figure 11, section 1103.

In the original application filed August 20, 2003;

Page 1, Paragraph [0004], 1<sup>st</sup> line

Page 7, Paragraph [0016], 8th line

Page 8, Paragraph [0017], 21st line

Page 20, Paragraph [0052], 4<sup>th</sup> line (PTO error)

Page 20, Paragraph [0054], 1st-2nd lines (PTO error)

Page 26, Paragraph [0068], 9<sup>th</sup> line

Page 26, Paragraph [0068], 21<sup>st</sup> line

Page 27, Paragraph [0070], 11<sup>th</sup> line

Page 28, Paragraph [0072], 15<sup>th</sup> line

Page 35, Paragraph [0087], 1st line (PTO error)

Page 47, Paragraph [0115], 6<sup>th</sup> line (PTO error)

4. Please send the Certificate to the undersigned at Customer No.: 22888.

Practitioner's Docket No. MST-012-1P

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5. Please pay the fee required by 37 C.F.R. § 1.20(a), as follows:

This document is being electronically filed and fees should be charged to our American Express credit card. Please charge any additional fees required by this paper or credit any overpayment in this matter to Deposit Account No. 50-0574 (Docket No. MST-012-1P).

Date: June 12, 2008 /E. Eric Hoffman/

Customer No.: 22888 E. Eric Hoffman Telephone No.: (925) 895-3545 Reg. No. 38,186

Bever, Hoffman & Harms, LLP

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page <u>1</u> of <u>2</u>

PATENT NO. : 7,353,438
APPLICATION NO. : 10/645,861
DATED : 4/1/2008

INVENTOR(S) : Wingyu Leung, et al.

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page, FOREIGN PATENT DOCUMENTS, add -- GB 2289779 11/1995 -- .

Column 1, line 30, amend "Semiconductors" to -- Semiconductor -- .

Column 4, line 6, amend "stuck at" to -- stuck-at -- .

Column 4, line 28, amend "stuck at" to -- stuck-at --.

Column 9, line 66, replace "1" with a comma.

Column 10, lines 27-28, delete "The logic high RESET signal also sets the read pointer value RP provided by toggle flip-flop **313** to a logic."

Column 12, line 66, insert -- 102 -- after "sequencer".

Column 13, line 11, amend "doe" to -- does -- .

Column 13, line 38, insert -- to -- before "have" .

Column 13, line 66, delete "the" (first occurrence).

Column 17, line 33, amend "I11" to -- 111 -- .

Column 22, line 54, amend "So" to -- S0 -- .

### MAILING ADDRESS OF SENDER

E. Eric Hoffman, Esq. 1432 Concannon Blvd., Building G Livermore, CA 94550-6006 Telephone: (925) 895-3545

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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# UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page <u>2</u> of <u>2</u>

PATENT NO. : 7,353,438 APPLICATION NO. : 10/645,861 DATED : 4/1/2008

INVENTOR(S) : Wingyu Leung, et al.

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

	× 1101
SELECT ONE OF THE 64-BIT WORDS IN	THE 64-BIT BIT MARCH TEST PATTERN
	y 1102
	PONSE TO THE 64-BIT WORD SELECTED PARITY-MATRIX OF FIG. 8)
	/ 1103
72-BIT STORAGE WORD THAT MIMICS A	) AND THE 8-BIT ECC WORD TO FORM A 72-BIT STORAGE WORD TO BE STORED ARRAY 101
	/ 1104
	WORD WITH A 72-BIT STUCK-AT-ZERO PATTERN HAVING A STUCK-AT-ZERO JLT
	1105
LOGICALLY FOR THE DATA PATTERN W 72-BIT STUCK-AT-ONE FAULT MASK HAVING STUCK-AT-FAULTS IN EITHER BO	TO CREATE A FAULTY 72-BIT WORD THE DATA WORD, THE ECC WORD, OR
	, 1106
GENERATE AN 8-BIT SYNDROME WORK WO	
<b>.</b>	× 1107
DECODE THE 8-BIT SYNDROME WORD RESULTS, THEN TOGGLE THE CORRE WORD. THEREBY CREATING	SPONDING BIT IN THE 72-BIT FAULTY
	/ 110 <del>8</del>
COMPARE THE CORRECTED 64-BIT WO MISMATCH IS DETECTED, CLASSIFY OBSER	THE FAULT AS CONTROLLABLE AND
	/ 1109
CHANGE THE FAULT MASKS IN STEPS 11 1108 UNTIL ALL MULTIPLE BI	
	/ 1110
CHANGE THE DATA WORD IN STEP 110 ALL 128 DATA PATTE	
•	/ 1111
IF ANY FAULT INTRODUCED TO ANY DA 1108, THEN THE FAULT COVERAGE OF T AND THE TEST P	HE BIT-MARCH PATTERN IS NOT 100%
FIG	. 11

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